FIG.1

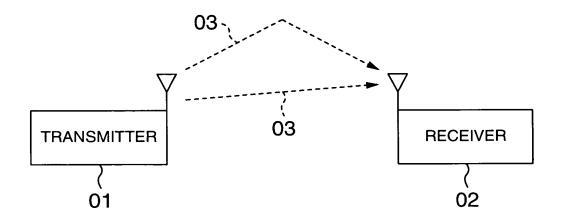
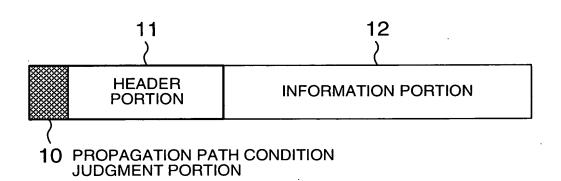
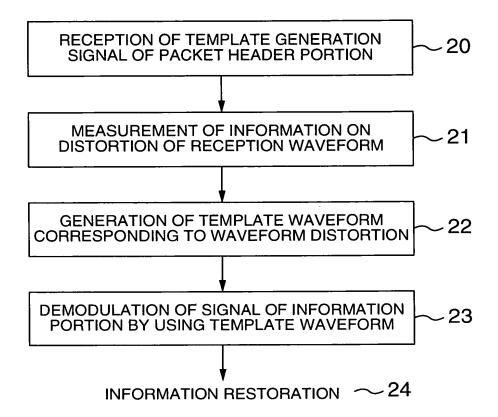


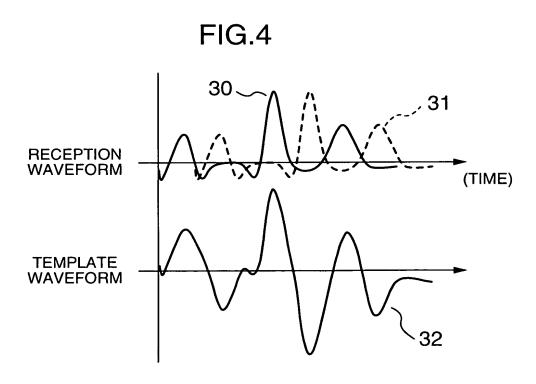
FIG.2

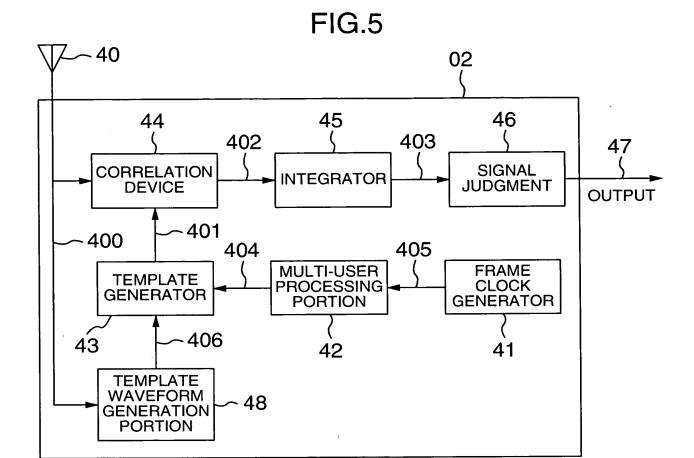


·/- .

FIG.3







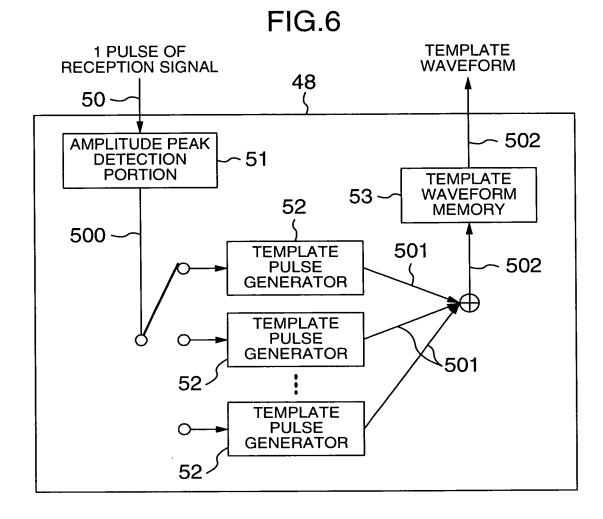
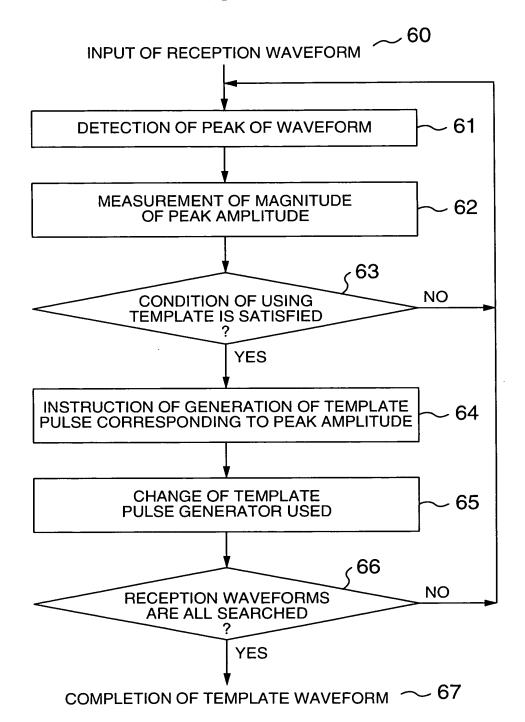


FIG.7



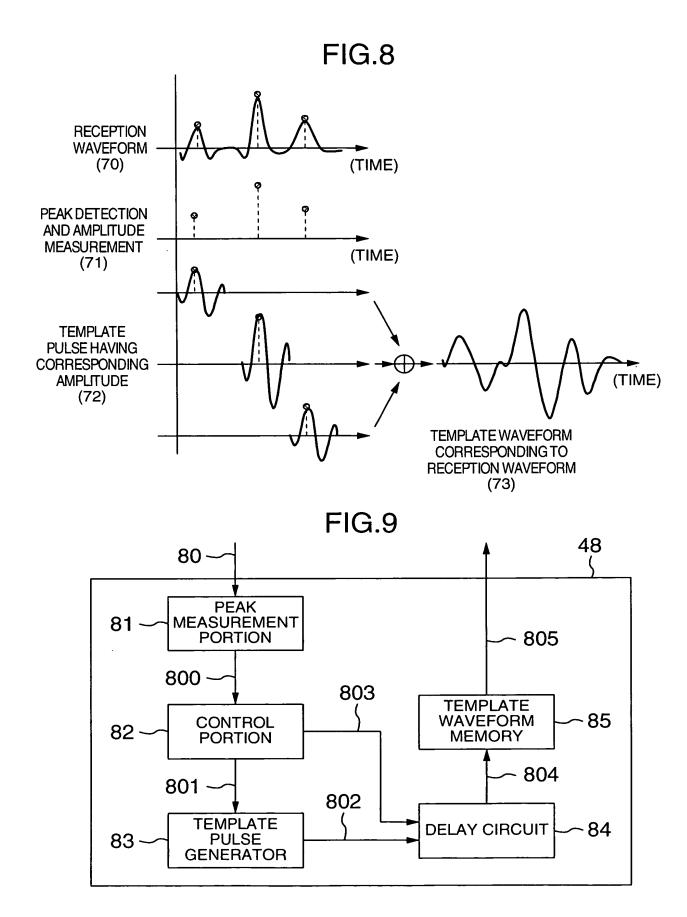


FIG.10

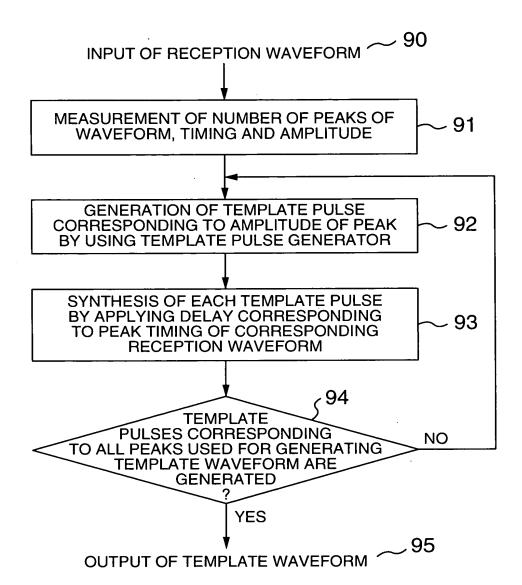


FIG.11 48 -1000 -1003 **TEMPLATE** 100 -**WAVEFORM** A/D - 104 **MEMORY** 1001-1003~ 1001 SYNTHETIC SIGNAL **WAVEFORM WAVEFORM** - 103 101 GENERATION RECORDING **PORTION PORTION** 1002 ~ 1001-WAVEFORM - 102 CONVERSION PORTION

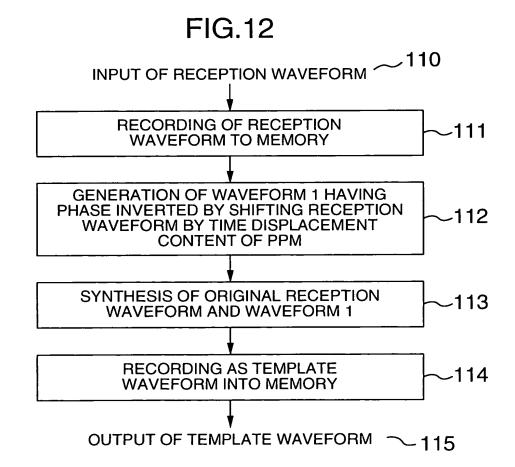


FIG.13

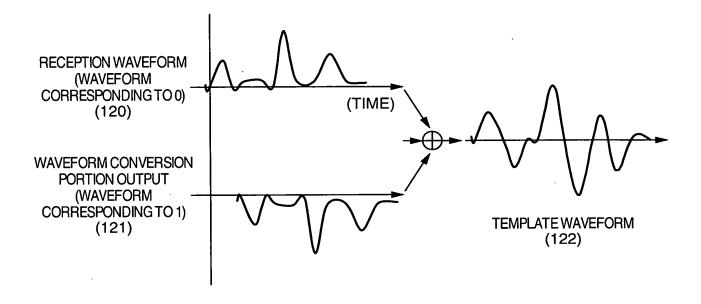


FIG.14

